

SEMICONDUCTOR COMPONENT HAVING FIELD-SHAPING REGIONS

5 Cross-Reference to Related Application:

This application is a continuation of copending International Application No. PCT/DE99/02732, filed September 1, 1999, which designated the United States.

10 Background of the Invention:

Field of the Invention:

15 The invention relates to a semiconductor component having a semiconductor body of at first conductivity type, that has a doping concentration of more than 5×10^{13} charge carriers per cm^3 and is provided with in each case at least one electrode on two mutually opposite surfaces. At least one of the electrodes makes contact with a semiconductor zone of a second conductivity type opposite to the first conductivity type. The invention relates in particular to an edge structure for such
20 a semiconductor component.

The minimum basic doping of silicon power components is about 8×10^{12} charge carriers cm^{-3} . In the case of such a basic doping, the bulk reverse voltage of a semiconductor body
25 having a pn junction is of the order of magnitude of 10 kV at room temperature.

In the case of monocrystalline silicon, the intrinsic carrier density at 300°K, that is to say at room temperature, of thermally generated electron-hole pairs is about 1.38×10^{10} charge carriers cm^{-3} . Such a concentration of the order of magnitude of 10^{10} charge carriers cm^{-3} is usually negligible relative to the basic doping of 8×10^{12} charge carriers cm^{-3} , which is virtually three orders of magnitude higher than the intrinsic charge carrier density.

It should be taken into consideration, however, that the intrinsic density rises exponentially with temperature, with the result that, in the case of a low doping concentration, the intrinsic charge carrier density can increase to a value corresponding to the charge carrier density provided by the basic doping. Thus, by way of an example, an intrinsic density which is reached at a temperature as low as 150°C (in this case "intrinsic temperature") corresponds to a basic doping of about 1×10^{13} charge carriers cm^{-3} . In other words, if the temperature of the semiconductor component lies in the intrinsic range, then the reverse current is drastically increased on account of the thermally generated electron-hole pairs.

These physical specifications inherently restrict the possibilities for using semiconductor components in power

engineering, in which voltages of up to 20 kV or more occur.

In order nevertheless to be able to control such high

voltages, series circuits including a plurality of

semiconductor components are used. Moreover, care is taken to

5 ensure that, in the case of semiconductor components having a low basic doping of, for example, less than $1 \times 10^{13} \text{ cm}^{-3}$ charge carriers in silicon, the maximum operating temperature is not allowed to exceed about 100°C , in order thus to avoid high reverse currents on account of a rising intrinsic density.

European Patent No. EP-B1-0 344 514 discloses a defeatable thyristor having a semiconductor body, which has an n-type emitter layer with an adjoining p-type base layer, a cathodal electrode making contact with the n-type emitter layer, and a p-type emitter layer with an adjoining n-type base layer, an anodal electrode making contact with the p-type emitter layer. The base layers are isolated from one another by a pn junction subject to a reverse-biasing of the thyristor in the blocking state. One of the base layers is provided with a gate
15 electrode to which a turn-off voltage pulse can be fed which causes the thyristor to turn off. At least one semiconductor layer is inserted into the base layer with which the gate electrode does not make contact. This semiconductor layer is not connected to external potentials, runs essentially
20 parallel to the pn junction, is oppositely doped with respect to the base layer and is thin in comparison with the thickness
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of the semiconductor body. The distance of the semiconductor layer from the pn junction is chosen to be so small so that the maximum field strength of the space charge zone (depletion zone) which builds up at the pn junction when the thyristor is
5 turned off is limited to a value below a critical value which results in an avalanche breakdown with regard to the charge carriers to be depleted in the event of turn-off. The semiconductor layers which are inserted into a base layer and are oppositely doped with respect thereto are provided with continuous cutouts whose dimensions in the lateral direction are small relative to the thickness of the respectively adjoining space charge zones that build up when the thyristor is turned off. In this case, the cutouts may be disposed in such a way as to produce a lattice-shaped structure of the
10 semiconductor layers. The cutouts are intended to improve the ignition behavior of the thyristor.

Summary of the Invention:

It is accordingly an object of the invention to provide a
20 semiconductor component which overcomes the above-mentioned disadvantages of the heretofore-known components of this general type and which has a high intrinsic temperature and can thus avoid large reverse currents despite high applied voltages.

With the foregoing and other objects in view there is provided, in accordance with the invention, a semiconductor component, including:

5 a semiconductor body of a first conductivity type, the semiconductor body having a first doping concentration greater than 5×10^{13} charge carriers cm^{-3} and having a first surface and a second surface, the first and second surfaces being provided opposite from one another;

at least a first electrode being disposed on the first surface and at least a second electrode disposed on the second surface;

15 a semiconductor zone of a second conductivity type opposite to the first conductivity type;

a pn-junction formed between the semiconductor zone of the second conductivity type and the semiconductor body;

20 at least one of the first and second electrodes being in contact with the semiconductor zone of the second conductivity type;

25 semiconductor regions of the second conductivity type provided in the semiconductor body;

the semiconductor regions being disposed at a respective distance from the semiconductor zone of the second conductivity type such that the semiconductor regions surround the semiconductor zone of the second conductivity type like a well;

the semiconductor regions being interrupted in each case at at least one location by channels formed by the semiconductor body; and

the semiconductor regions of the second conductivity type having a second doping concentration that is high enough such that the semiconductor regions are not completely depleted of charge carriers in case of a reverse-biasing of the pn-junction.

In other words, the object of the invention is achieved by a semiconductor component having a semiconductor body of a first conductivity type, which has a doping concentration of above 5×10^{13} charge carriers cm^{-3} and is provided with in each case at least one electrode on two mutually opposite surfaces, in which case at least one of the electrodes makes contact with a semiconductor zone of a second conductivity type opposite to the first conductivity type, in which case regions of the second conductivity type are provided in the semiconductor

body, at a distance from the zone of the second conductivity type, the regions surrounding the zone of the second conductivity type in a well-like manner and being interrupted in each case at at least one location by channels of the semiconductor body and the regions of the second conductivity type are so highly doped that they are not completely depleted of charge carriers in the case of reverse-biasing of the pn junction formed between the zone of the second conductivity type and the semiconductor body.

The invention thus provides a semiconductor component having a semiconductor body of the first conductivity type. Regions of the second conductivity type are embedded in this semiconductor body of the first conductivity type in such a way that there are an arbitrary number of zones of the first conductivity type in the semiconductor body, which have a basic doping which is greater than 5×10^{13} charge carriers cm^{-3} . These zones of the first conductivity type are connected to one another via channels which lead through the regions of the second conductivity type. The doping concentration in the regions of the second conductivity type is defined in such a way that these regions are not completely depleted of charge carriers in the case of reverse-biasing of the semiconductor component.

The regions of the second conductivity type are preferably provided at a distance from the zone of the second conductivity type and in a well-shaped manner around the zone of the second conductivity type and are interrupted by the channels of the first conductivity type. Electric fields having a trapezoidal profile build up in the zones of the first conductivity type in the case of reverse-biasing, the length of such a "field strength trapezoid" being defined by the length of the respective zones of the first conductivity type, that is to say by the distance between two adjacent regions of the second conductivity type. By increasing the number of regions of the second conductivity type which are in each case interrupted by the channels of the first conductivity type which connect the zones of the first conductivity type to one another, it is possible to string together as many "field strength trapezoids" as desired, which leads to a corresponding increase of the reverse voltage.

As a result of the higher basic doping in the semiconductor body, that is to say in the zones of the first conductivity type, the intrinsic temperature is significantly higher than in the case of a homogeneously doped semiconductor body, when a comparable reverse voltage is present in each case. This distinctly alleviates the abovementioned restriction with regard to the operating temperature below a maximum of 100°C.

In the case of the semiconductor component according to the invention, then, the semiconductor body is configured by series-connecting "field strength trapezoids" in such a way that practically any desired reverse voltages can be achieved, without further reducing the basic doping of the semiconductor body and hence also the intrinsic temperature. This series connection of "field strength trapezoids" is comparable in the case of edge structures with field ring systems which act as voltage dividers for the space charge zone.

The regions of the second conductivity type are preferably configured in such a way that the space charge zones of the individual field strength trapezoids reach the surface of the semiconductor body or as far as the nearest "layer" of the regions of the second conductivity type which are provided in the well-shaped manner, in order to avoid, in the bulk of the semiconductor body, peak values of the electric field which lie above the critical field strength.

The channels of the first conductivity type which are routed between the regions of the second conductivity type connect, as has been explained above, the zones of the first conductivity type which are created by these regions, with the result that the operating current can flow through the channels. These channels, then, should be configured in such a way that no spikes of the electric field occur in them.

An insulating zone, for example including a silicon dioxide layer, may additionally be incorporated in the semiconductor body, in order to shield charge carriers from the edge region of the semiconductor component.

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The invention can preferably be applied to diodes, thyristors, transistors, such as MOS transistors or IGBTs (Insulated Gate Bipolar Transistors) and other known semiconductor power components.

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According to another feature of the invention, the semiconductor regions of the second conductivity type are interrupted in each case at a plurality of locations by the channels formed by the semiconductor body for increasing a reverse voltage.

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According to yet another feature of the invention, the channels are configured such that electric field spikes are avoided when a reverse voltage is applied between the first and second electrodes.

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According to a further feature of the invention, the semiconductor body has a drift region between the electrodes; and the channels are provided in the drift region.

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According to yet a further feature of the invention, the semiconductor body has an edge region; and the channels are provided in the edge region.

- 5 According to another feature of the invention, the semiconductor body has an edge zone; and an insulating zone is provided for shielding charge carriers from the edge zone.

According to a further feature of the invention, an injector is disposed in at least one of the first and second surfaces.

According to an additional feature of the invention, one of the first and second surfaces surrounds the semiconductor zone of the second conductivity type; and field plates are provided on this surface.

According to another feature of the invention, the semiconductor body has an edge; and a doped guard ring zone of the first conductivity type surrounds the edge.

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According to yet another feature of the invention, the first conductivity type is an n-conductivity type.

With the objects of the invention in view there is also

- 25 provided, a semiconductor configuration, including:

a semiconductor component such as a diode, a MOS transistor or a thyristor;

the semiconductor component including:

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a semiconductor body of a first conductivity type, the semiconductor body having a first doping concentration greater than 5×10^{13} charge carriers cm^{-3} and having a first surface and a second surface, the first and second surfaces being provided opposite from one another;

at least a first electrode being disposed on the first surface and at least a second electrode disposed on the second surface;

a semiconductor zone of a second conductivity type opposite to the first conductivity type;

a pn-junction formed between the semiconductor zone of the second conductivity type and the semiconductor body;

at least one of the first and second electrodes being in contact with the semiconductor zone of the second conductivity type;

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semiconductor regions of the second conductivity type provided in the semiconductor body;

the semiconductor regions being disposed at a respective
5 distance from the semiconductor zone of the second conductivity type such that the semiconductor regions surround the semiconductor zone of the second conductivity type like a well;

10 the semiconductor regions being interrupted in each case at at least one location by channels formed by the semiconductor body; and

15 the semiconductor regions of the second conductivity type having a second doping concentration which is high enough such that the semiconductor regions are not completely depleted of charge carriers in case of a reverse-biasing of the pn-junction.

20 Other features which are considered as characteristic for the invention are set forth in the appended claims.

Although the invention is illustrated and described herein as embodied in a semiconductor component having field-shaping
25 regions, it is nevertheless not intended to be limited to the details shown, since various modifications and structural

changes may be made therein without departing from the spirit of the invention and within the scope and range of equivalents of the claims.

5 The construction and method of operation of the invention, however, together with additional objects and advantages thereof will be best understood from the following description of specific embodiments when read in connection with the accompanying drawings.

Brief Description of the Drawings:

Fig. 1 is a diagrammatic sectional view of a diode having a patterned semiconductor body and regions of the second conductivity type which are continuous in the edge region;

Fig. 2 is a diagrammatic sectional view of a diode having a patterned semiconductor body and regions of the second conductivity type which are interrupted in the edge region;

20 Fig. 3 is a diagrammatic sectional view of a diode having a patterned semiconductor body and regions of the second conductivity type which are continuous in the edge region, and also having an incorporated insulating zone for shielding the charge carriers from the edge region;

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Fig. 4 is a diagrammatic sectional view of a MOS field-effect transistor having a patterned semiconductor body and regions of the second conductivity type which are interrupted in the edge region;

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Fig. 5 is a diagrammatic sectional view of a MOS field-effect transistor having a patterned semiconductor body and regions of the second conductivity type which are interrupted in the edge region, but, in contrast to the MOS field-effect transistor of Fig. 4, no field plates are provided; and

Fig. 6 is a diagrammatic sectional view of a MOS field-effect transistor having an injector.

Description of the Preferred Embodiments:

In the figures, corresponding parts are respectively provided with the same reference symbols. Moreover, in the figures not all of the sectional parts are hatched, for the sake of better illustration. It is also noted that the respective conductivity types illustrated in the exemplary embodiments can, of course, be reversed.

Referring now to the figures of the drawings in detail and first, particularly, to Fig. 1 thereof, there is shown a semiconductor body 1 including an n^+ -conducting semiconductor

substrate 2 and an n^- -conducting semiconductor layer 3 having a basic doping which is 5×10^{13} charge carriers cm^{-3} or more.

A cathode K is connected to the semiconductor substrate 2,
5 while an anode A is connected to a p^+ -conducting zone 4.

Field plates 5 are provided on the top side of the semiconductor body 1 on or in an insulating layer (not specifically illustrated) made of silicon dioxide, for example. Moreover, an n^+ -conducting guard ring 6 is additionally embedded in that surface of the semiconductor body 1 which faces the field plates 5. This guard ring 6, like the field plates 5, serves for increasing the breakdown strength of the diode in the edge region thereof.

The invention then provides p -conducting regions 7, which surround the zone 4 at a distance in a well-shaped manner and are interrupted by channels 8 in the drift region below the zone 4, through which channels the operating current can flow
20 between anode A and cathode K.

The regions 7 are so highly doped that they are not completely depleted of charge carriers in the case of reverse-biasing when the reverse voltage is applied between anode A and
25 cathode K.

Fig. 2 shows a further exemplary embodiment of the invention, which differs from the exemplary embodiment of Fig. 1 by the fact that here the regions 7 are interrupted in the edge region, too, by a multiplicity of channels 8. A "field strength trapezoid" is also indicated here diagrammatically on one of the channels 8, by a broken line 9. The length of this field strength trapezoid is determined by the length of the semiconductor body 1 between the regions 7, that is to say by the width of the channel 8 of the first conductivity type. In other words, the length of the field strength trapezoid depends on the distance between two adjacent regions of the second conductivity type. By correspondingly increasing the number of regions 7 of the second conductivity type, it is thus possible to string together practically any desired number of field strength trapezoids, which leads to a corresponding increase of the reverse voltage.

Fig. 3 shows a further exemplary embodiment of the invention, in which - in a similar manner to the exemplary embodiment of Fig. 1 - the p-conducting regions 7 are continuous in the edge region, with the result that there are channels 8 only in the drift region of the diode. Moreover, an insulating zone 10 made of silicon dioxide, for example, is additionally provided here, which insulating zone annularly surrounds the region below the zone 4 and shields charge carriers from the edge

region. Accordingly, field plates 5 are not provided in this exemplary embodiment.

Fig. 4 shows a MOS field-effect transistor having an emitter electrode E, a gate electrode G and a collector electrode C. The collector electrode C is connected to a p⁺-conducting zone 11, while the emitter electrode E is connected to a p-conducting zone 12 and an n-conducting zone 13 and the gate electrode G lies above the channel region formed by the zone 12 and is isolated in a customary manner from the semiconductor body by an insulating layer, for example, made of silicon dioxide. In this exemplary embodiment, in a similar manner to Fig. 2, the p-conducting regions 7 surround the zones 12 and 13 at a respective distance in a well-shaped manner and are in this case each isolated by channels 8 of the semiconductor body 2. Moreover field plates 5 for increasing the breakdown strength are additionally provided in the edge region of this MOS field-effect transistor.

Fig. 5 shows a further exemplary embodiment of the invention with a MOS field-effect transistor which corresponds to the MOS field-effect transistor of Fig. 4 but has no field plates 5.

As is shown in Fig. 1 and 2, the field plates 5 can be connected to the p-conducting regions 5. However, they can also be floating, as is illustrated in Fig. 4.

5 Fig. 6 shows a further exemplary embodiment of the invention, in which the semiconductor body includes, instead of the semiconductor layer 3, a plurality of differently doped epitaxial layers 16, 17 and 18, between each of which the regions 7 are introduced for example by ion implantation. In
10 this connection, it should be noted that in the previous exemplary embodiments of Figs. 1 to 5, too, the individual regions 5 can be produced by corresponding deposition of individual, identically doped layers and ion implantation steps. Moreover, a source metallization layer 19 connected to
15 a terminal S, an insulating layer 20 made of silicon dioxide, gate electrodes 21 made, for example, of doped polycrystalline silicon and a p-conducting zone 22 are additionally shown in the exemplary embodiment of Fig. 6. Instead of this p-conducting zone 22, it is also possible to provide a weak
20 injector, for example a Schottky barrier layer. This zone 22 can have the same layer thickness as the substrate 2 or be thicker than the latter. Adjacent to the substrate layer 2 is a layer 14, which is connected to a terminal D.